

Electronic Acknowledgement Receipt

EFS ID:	3529651
Application Number:	10808341
International Application Number:	
Confirmation Number:	5441
Title of Invention:	DIGITAL SIGNAL PROCESSING ARCHITECTURE WITH A WIDE MEMORY BANDWIDTH AND A MEMORY MAPPING METHOD THEREOF
First Named Inventor/Applicant Name:	Hyun-kyu Yun
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Attorney Docket Number:	101-1019
Receipt Date:	27-JUN-2008
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Issue Fee Payment (PTO-85B)	Issue_Fee.pdf	118146	no	1
			45a50fc07752b26f856d242904e832fe9a759deb		

Warnings:

Information:

2	Fee Worksheet (PTO-06)	fee-info.pdf	8352	no	2
			d238e75cbb42c78e9bd4868f3906ca264aa880c8		

Warnings:

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Total Files Size (in bytes):			126498
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